

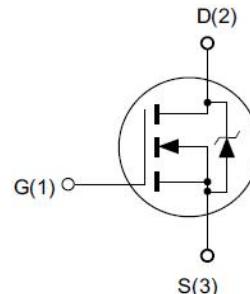


# MPD07N65

## N-Channel Power MOSFET

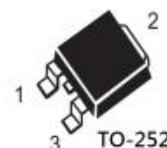
### Features

- ◆ 650V, 7A,  $R_{DS(ON)}$ (Max.) = 1.4Ω@VGS = 10V.
- ◆ Low Crss
- ◆ Fast Switching
- ◆ 100% Avalanche Tested



### Application

- ◆ Adaptor
- ◆ Standby Power
- ◆ Switching power supply
- ◆ LED Power



### Absolute Maximum Ratings $T_c = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Limit	Unit
		TO-252	
$V_{DS}$	Drain-Source Voltage <sup>a</sup>	650	V
$V_{GS}$	Gate-Source Voltage	$\pm 30$	V
$I_D$	Drain Current-Continuous, $T_c = 25^\circ\text{C}$	7	A
	Drain Current-Continuous, $T_c = 100^\circ\text{C}$	4.4	A
$I_{DM}$	Drain Current-Pulsed <sup>b</sup>	28	A
$P_D$	Maximum Power Dissipation @ $T_J = 25^\circ\text{C}$	100	W
$dv/dt$	Peak Diode Recovery $dv/dt$ <sup>c</sup>	5.0	V/ns
$E_{AS}$	Single Pulsed Avalanche Energy <sup>d</sup>	320	mJ
$T_J, T_{STG}$	Operating and Store Temperature Range	-55 to 150	°C

### Thermal Characteristics

Symbol	Parameter	Value	Unit
$R_{\theta Jc}$	Junction-to-Case	1.25	°C/W
$R_{\theta JA}$	Junction-to-Ambient	100	°C/W

### Electrical Characteristics $T_J = 25^\circ\text{C}$ unless otherwise noted

#### Off Characteristics

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS} = 0\text{V}, I_D = 250\mu\text{A}$	650	-	-	V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS} = 650\text{V}, V_{GS} = 0\text{V}$	-	-	1	μA
$I_{GSS}$	Forward Gate Body Leakage Current	$V_{DS} = 0\text{V}, V_{GS} = \pm 30\text{V}$	-	-	$\pm 100$	nA



# MPD07N65

## N-Channel Power MOSFET

### ■ On Characteristics

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}$ , $I_D = 250\mu A$	2	-	4	V
$R_{DS(on)}$	Static Drain-Source On-Resistance	$V_{GS} = 10V$ , $I_D = 3.5A$	-	1.1	1.4	$\Omega$

### ■ Dynamic Characteristics

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
$C_{iss}$	Input Capacitance	$V_{DS} = 25V$ , $V_{GS} = 0V$ , $f = 1.0MHz$	-	1130	-	pF
$C_{oss}$	Output Capacitance		-	93	-	pF
$C_{rss}$	Reverse Transfer Capacitance		-	5.5	-	pF

### ■ On Characteristics

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
$t_{d(on)}$	Turn-On Delay Time	$V_{DD} = 325V$ , $I_D = 7A$ , $V_{GS} = 10V$	-	19	-	ns
$t_r$	Turn-On Rise Time		-	21	-	ns
$t_{d(off)}$	Turn-Off Delay Time		-	42	-	ns
$t_f$	Turn-Off Fall Time		-	19	-	ns
$Q_g$	Total Gate Charge	$V_{DS} = 520V$ , $I_D = 7A$ , $V_{GS} = 10V$	-	24	-	nC
$Q_{gs}$	Gate-Source Charge		-	5.1	-	nC
$Q_{gd}$	Gate-Drain Charge		-	9.5	-	nC

### ■ Drain-Source Diode Characteristics

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
$I_s$	Drain-Source Diode Forward Continuous Current	$V_{GS} = 0V$	-	-	7	A
$I_{SM}$	Maximum Pulsed Current	$V_{GS} = 0V$	-	-	28	A
$V_{SD}$	Drain-Source Diode Forward Voltage	$V_{GS} = 0V$ , $I_s = 7A$	-	-	1.4	V
$T_{rr}$	Body Diode Reverse Recovery Time	$di/dt = 100A/us$ $I_s = 7A, V_{GS} = 0V$	-	380	-	ns
$Q_{rr}$	Body Diode Reverse Recovery Charge	$di/dt = 100A/us$ $I_s = 7A, V_{GS} = 0V$	-	1900	-	nC

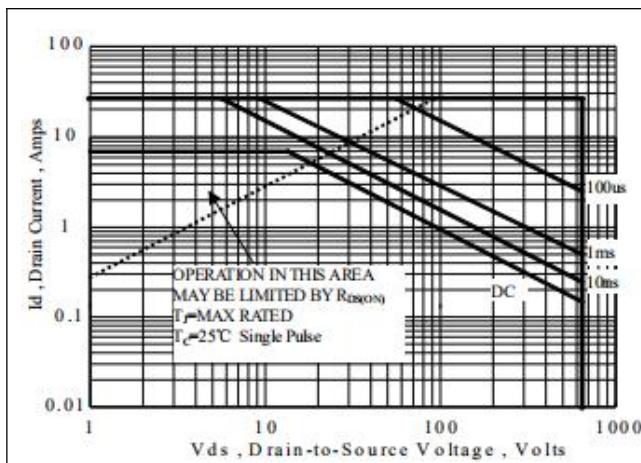
Notes:

a.  $T_J = +25^\circ C$  to  $+150^\circ C$

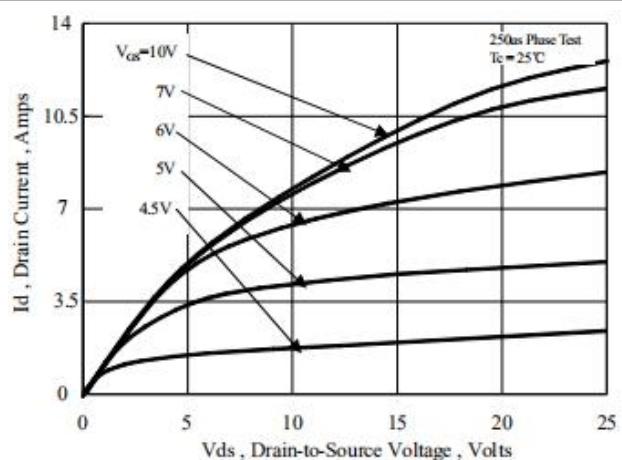
b. Repetitive rating; pulse width limited by maximum junction temperature.

c.  $I_{SD} = 7A$ ,  $di/dt \leq 100A/us$ ,  $V_{DD} \leq BV_{DS}$ , Start  $T_J = 25^\circ C$

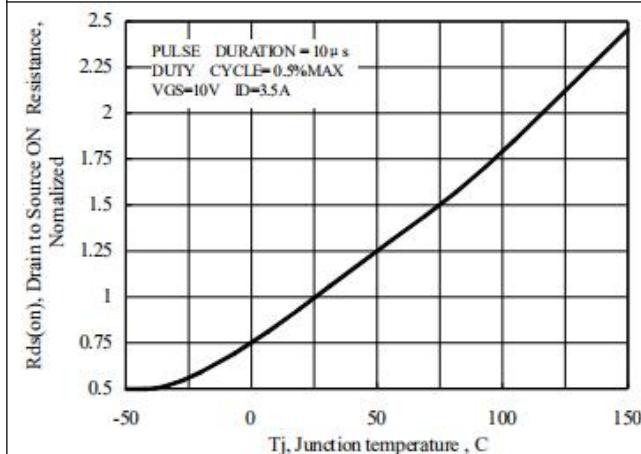
d.  $L = 10mH$ ,  $V_{DD} = 50V$ ,  $I_{AS} = 8.0A$ ,  $R_G = 25\Omega$  Starting  $T_J = 25^\circ C$



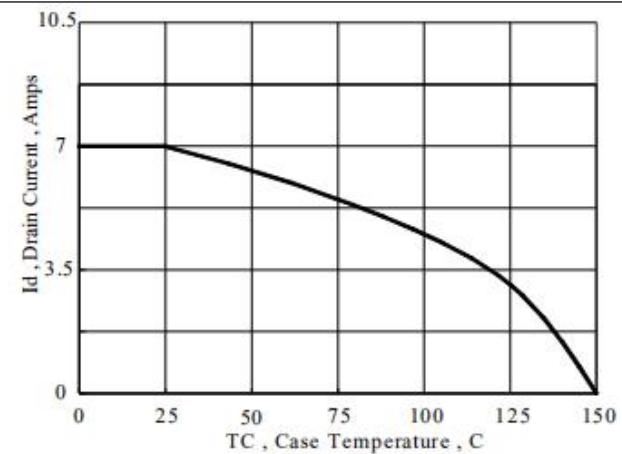
**Figure 1.** Maximum Forward Bias Safe Operating Area



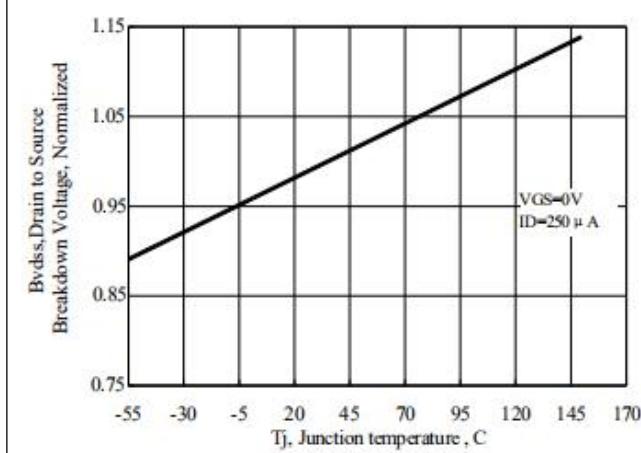
**Figure 2.** On-State Characteristics



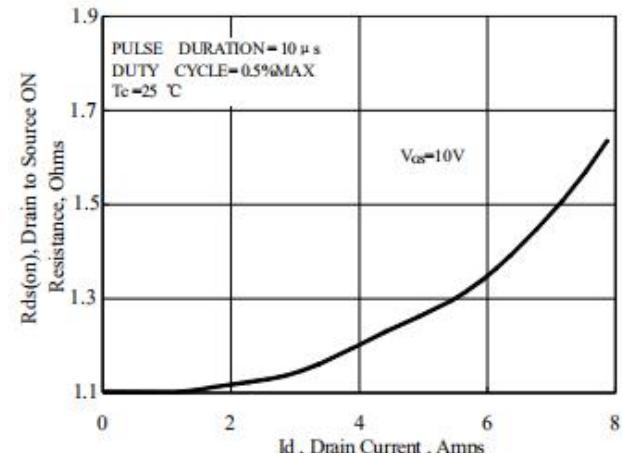
**Figure 3.** Normalized On-Resistance Variation with Temperature



**Figure 4.** Maximum Continuous Drain Current vs Case Temperature



**Figure 5.** Typical Breakdown Voltage vs Junction Temperature



**Figure 6.** Typical Drain to Source ON Resistance vs Drain Current

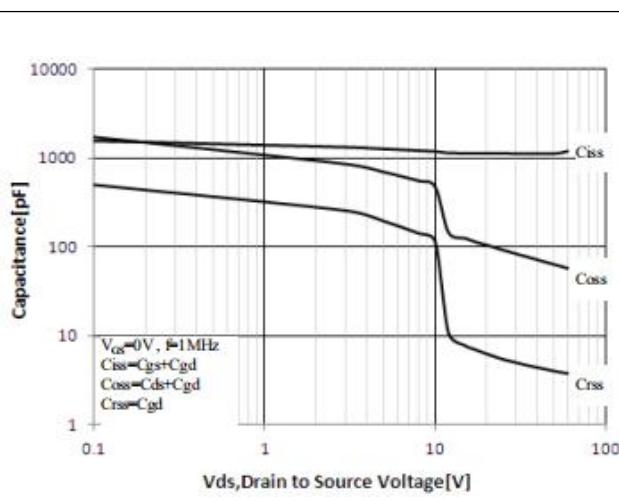


Figure 7. Typical Capacitance vs Drain to Source Voltage

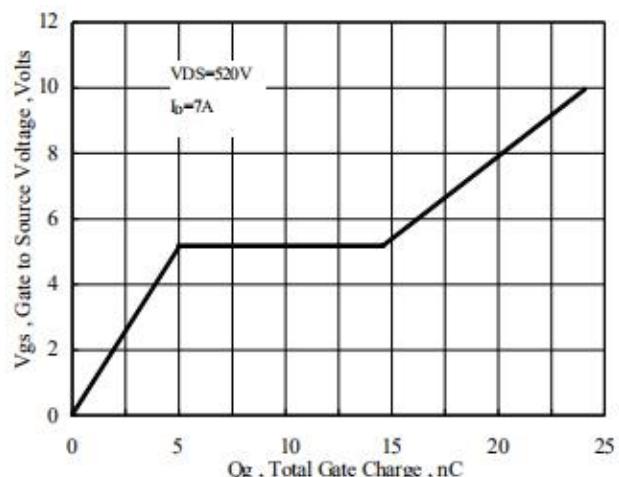


Figure 8. Typical Gate Charge vs Gate to Source Voltage

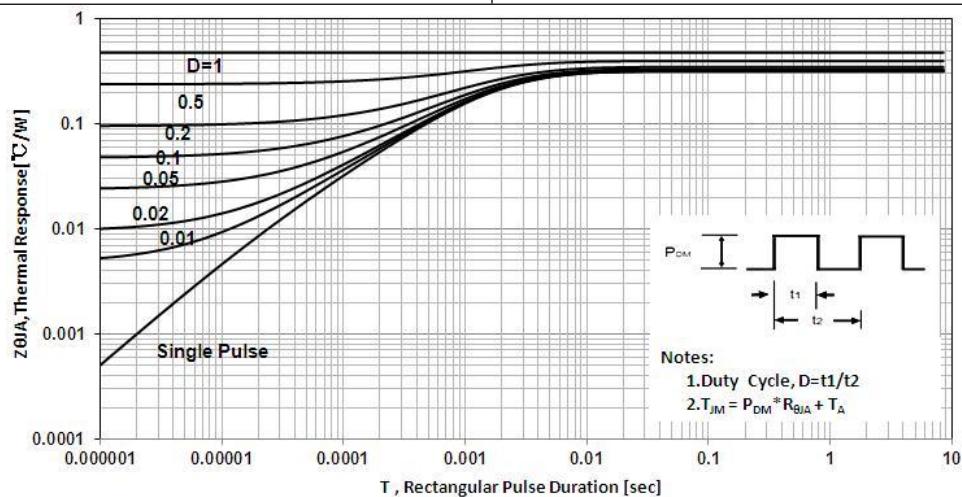


Figure 9 .Normalized Effective Transient Thermal Impedance With Pulse Duration(TO-252)



# MPD07N65

## N-Channel Power MOSFET

### ■ Package Information

